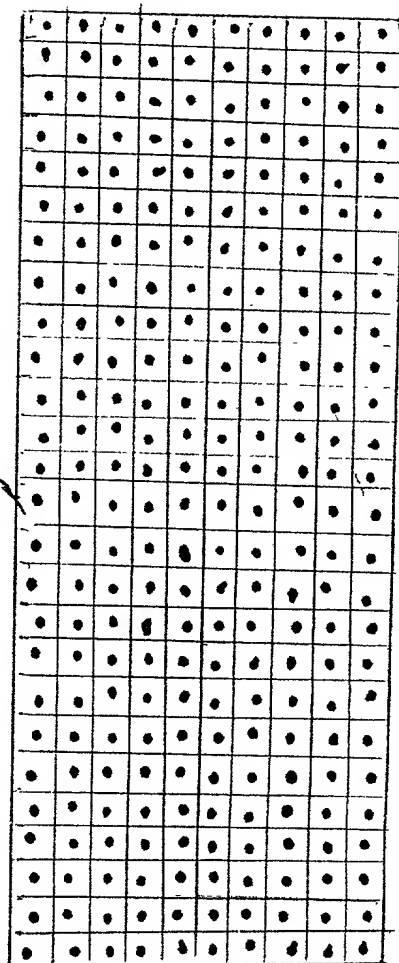


1

Fig. 2

11



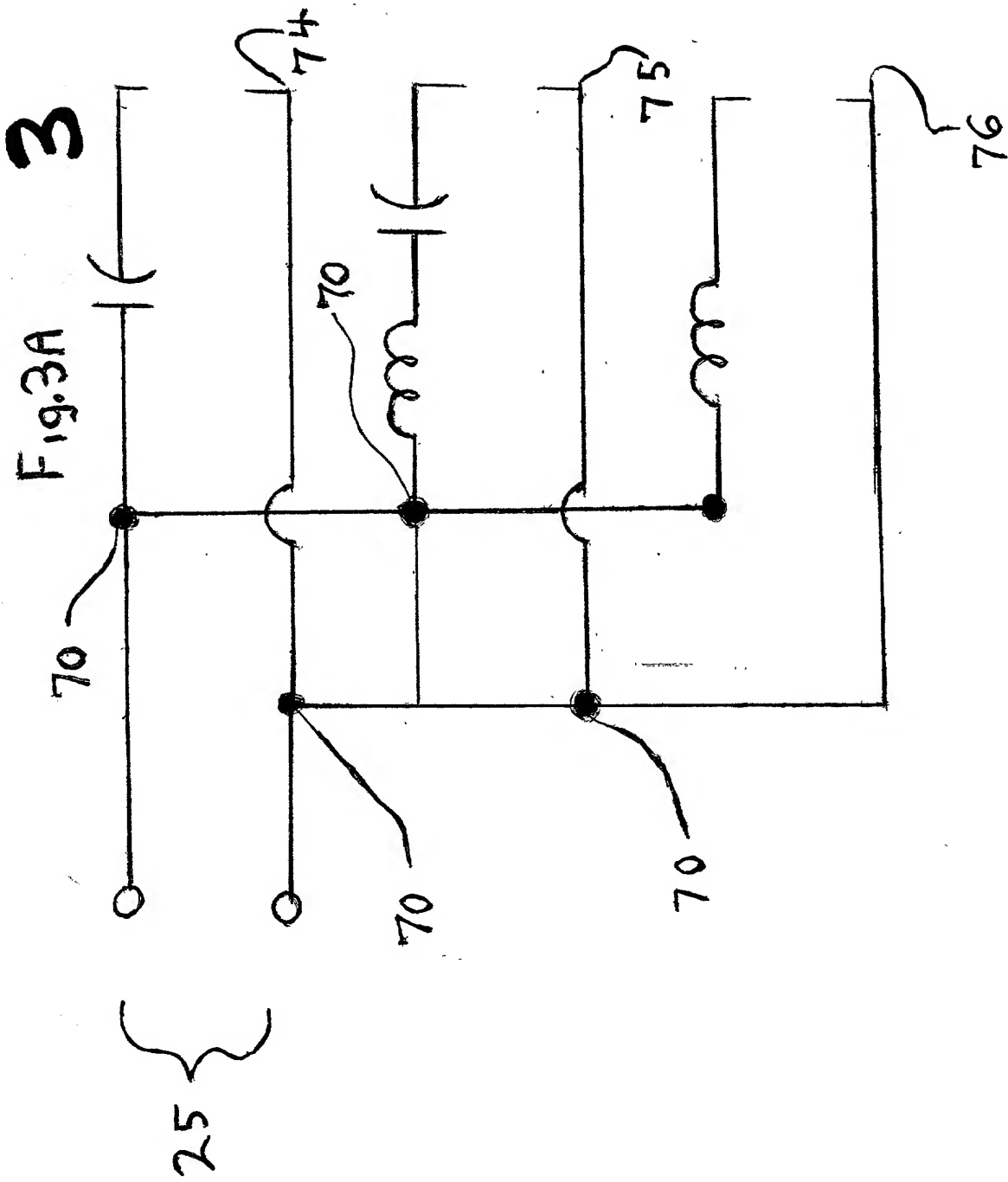


Fig. 3E

4

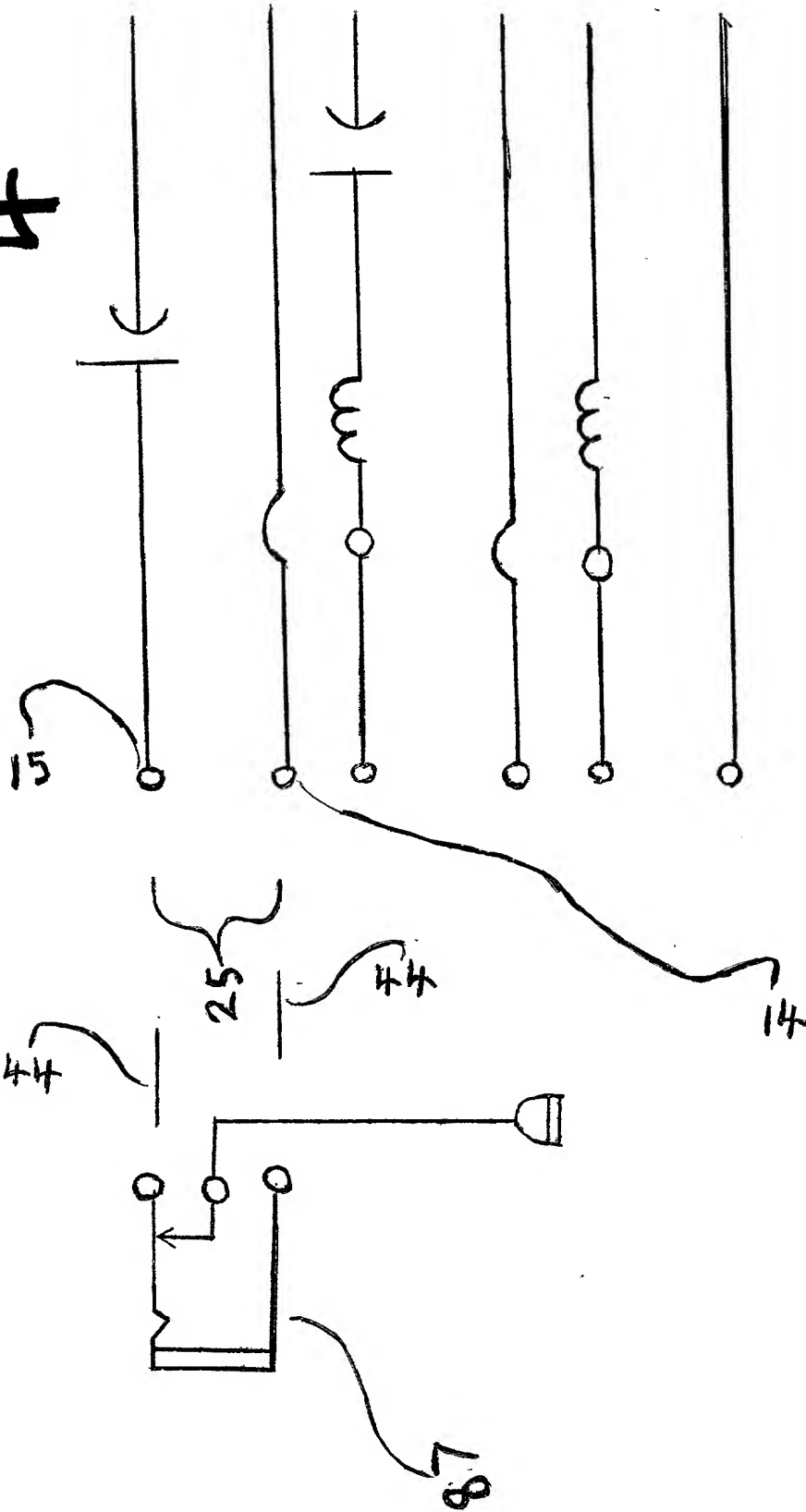
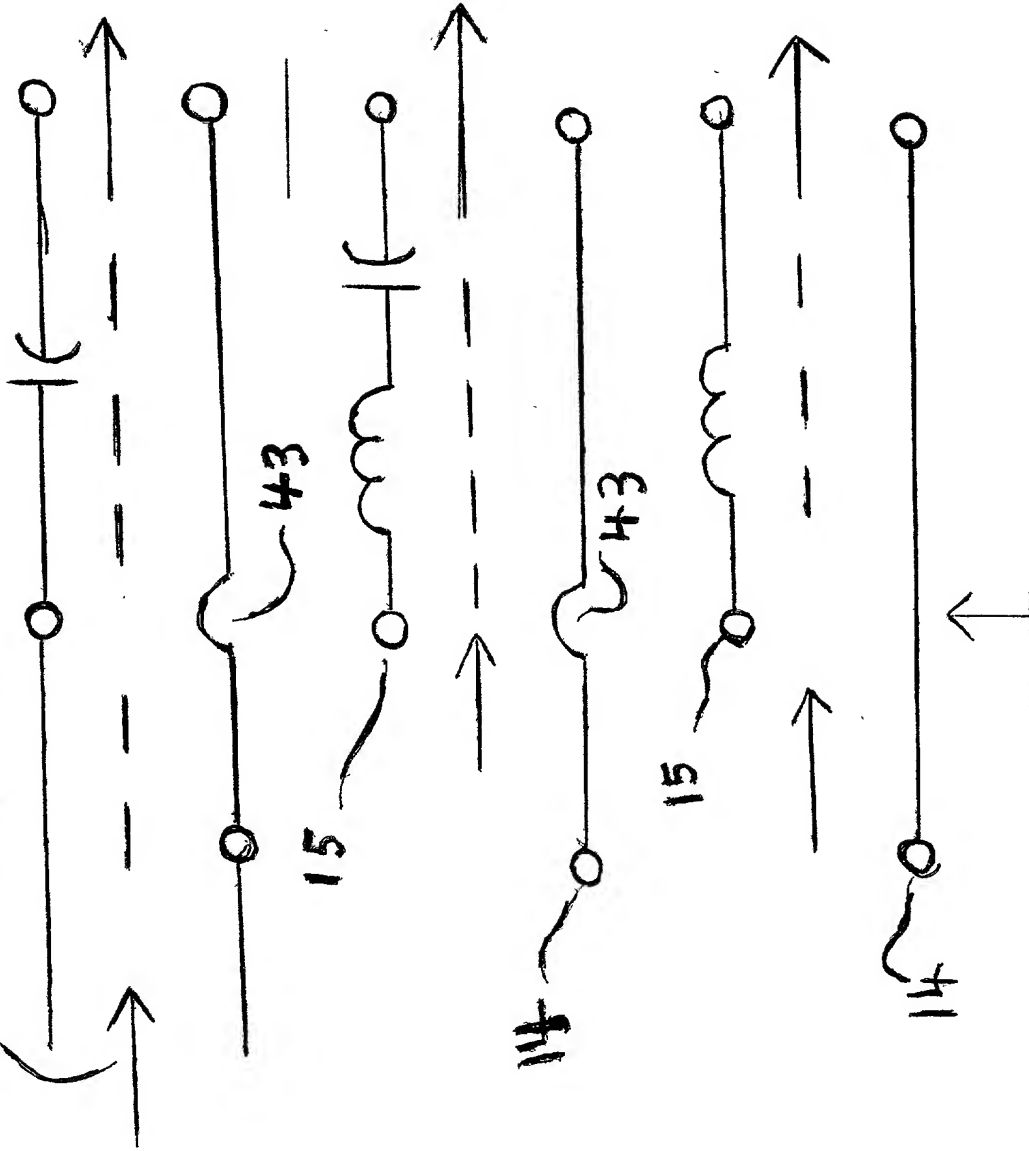


Fig. 3B 5



15 ✓ Fig. 3D

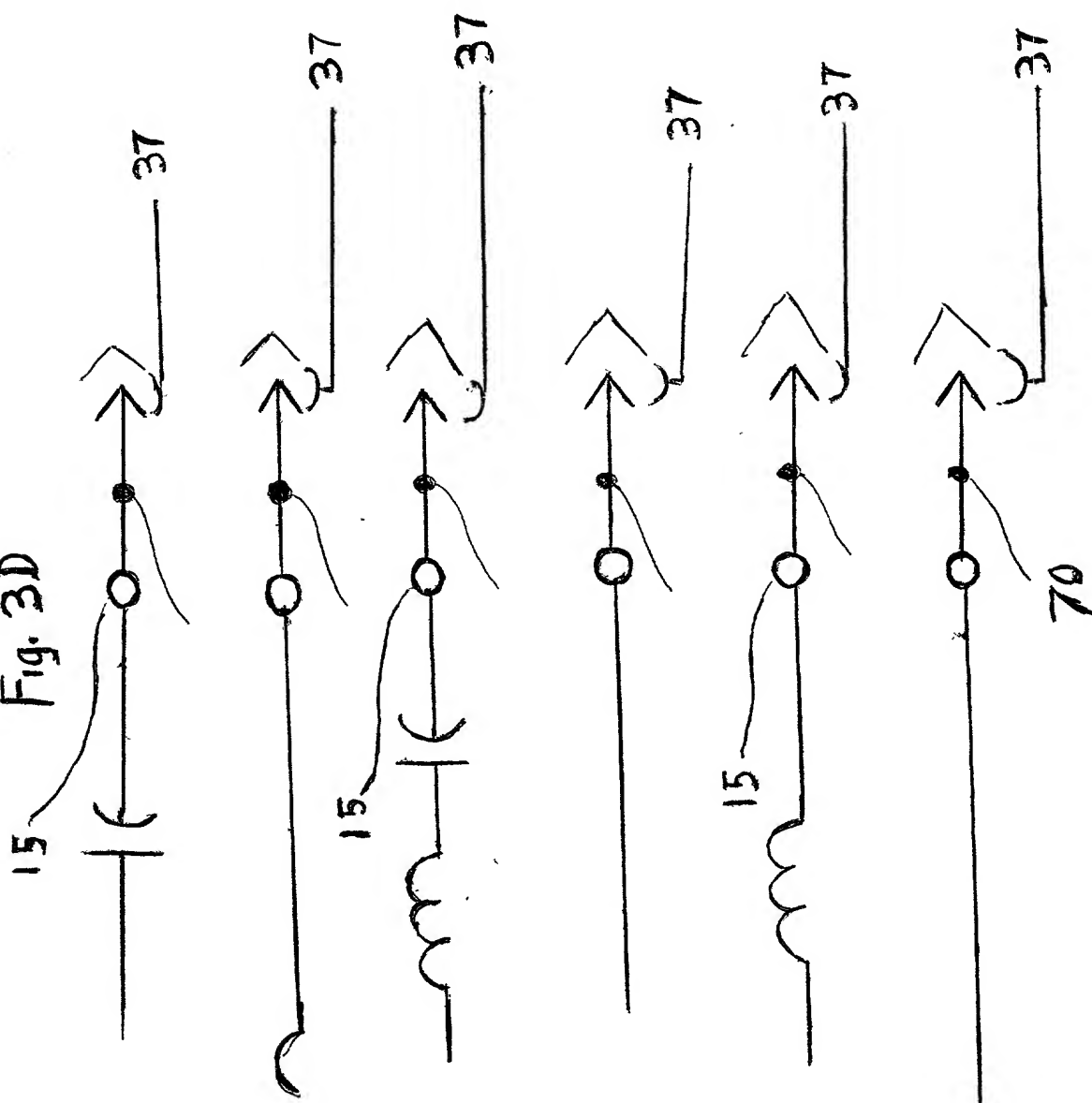


Fig. 4 F

7

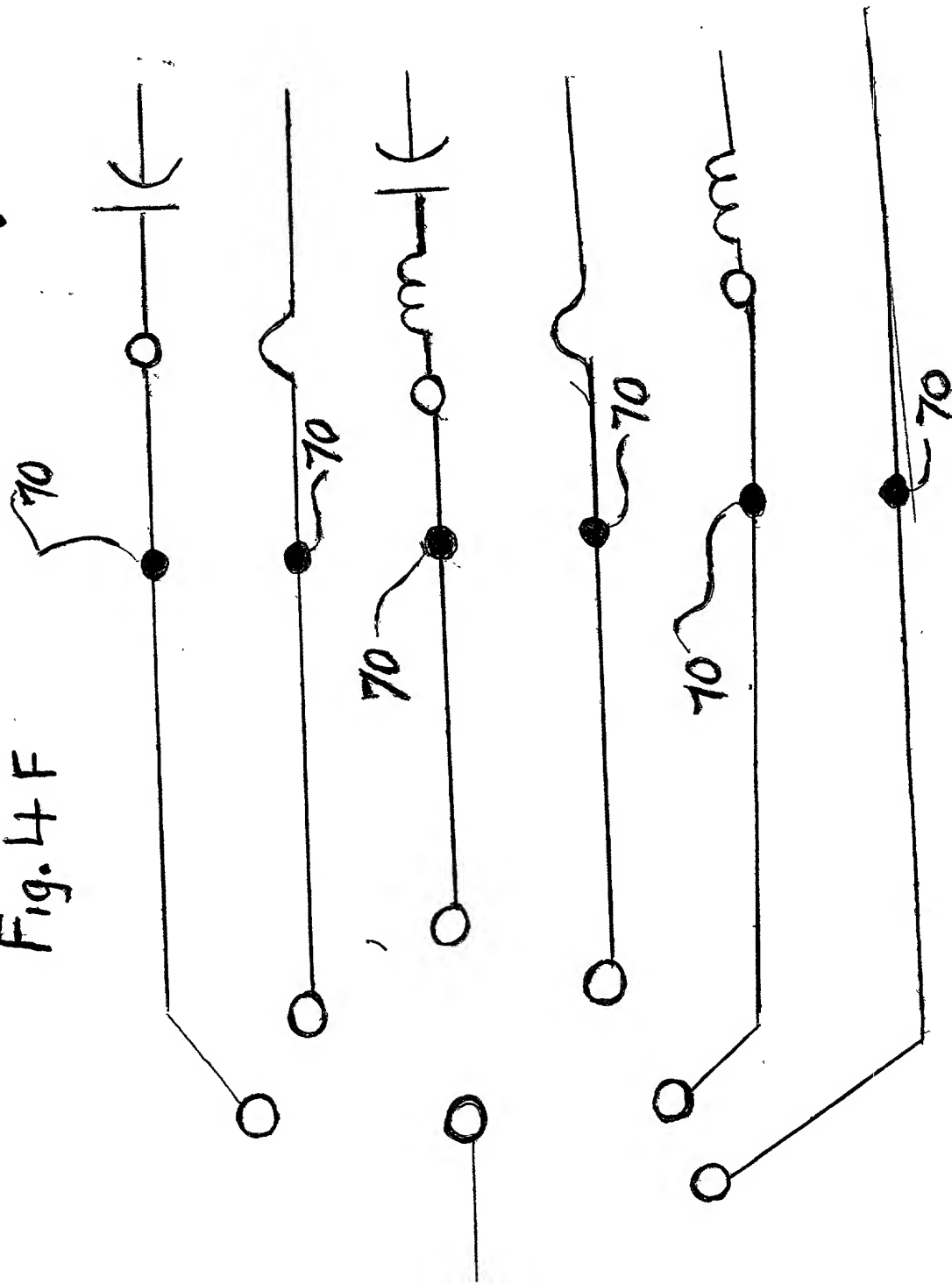
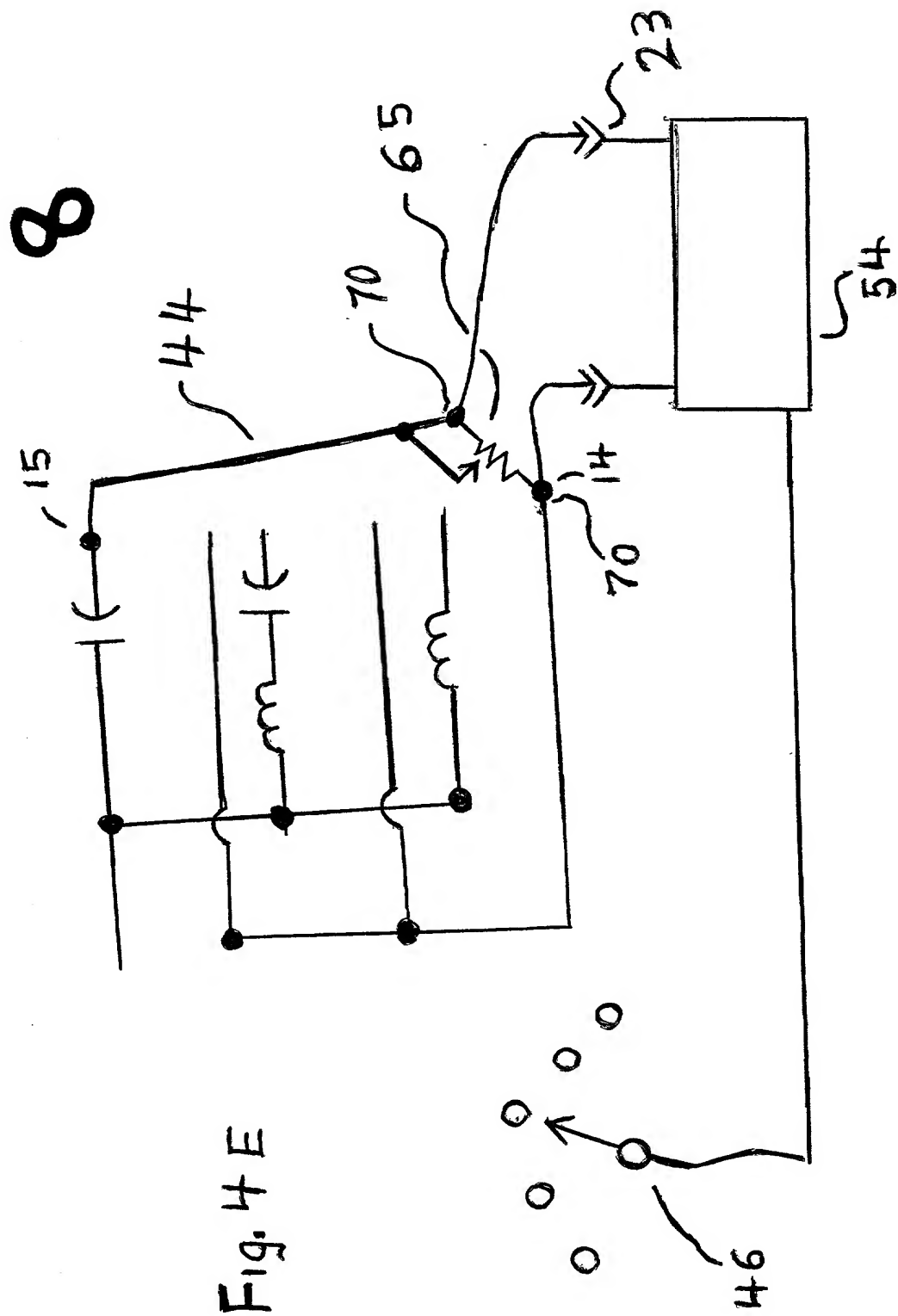


FIG. 4E is a schematic diagram of a circuit 8. The circuit includes a power source 15, a switch 44, a resistor 70, a capacitor 46, and a load 54. The power source 15 is connected to the switch 44, which is in series with the resistor 70. The resistor 70 is connected to the capacitor 46, which is in parallel with the load 54. The circuit is labeled 8.



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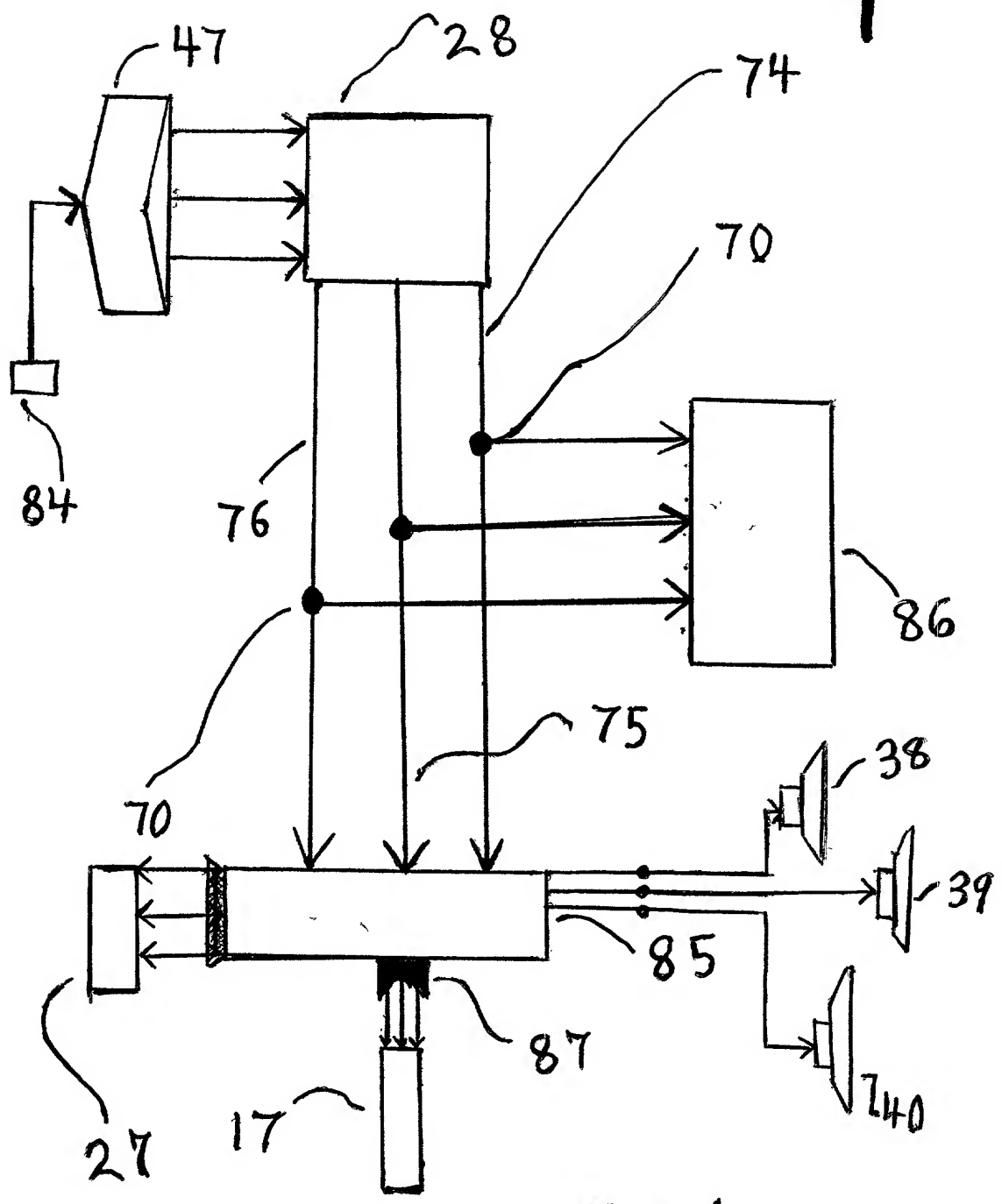


Fig. 1

Fig. 3C

01

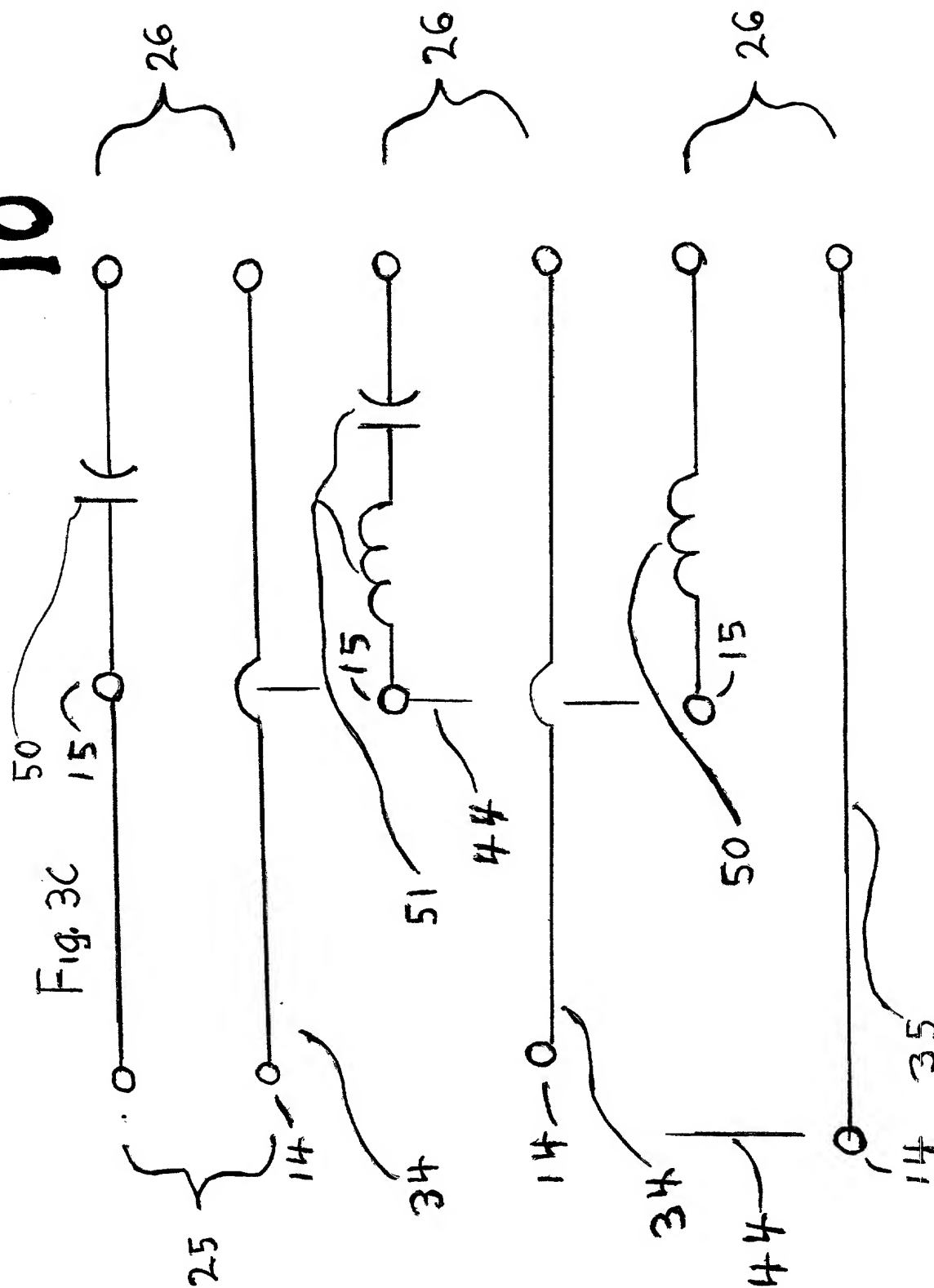
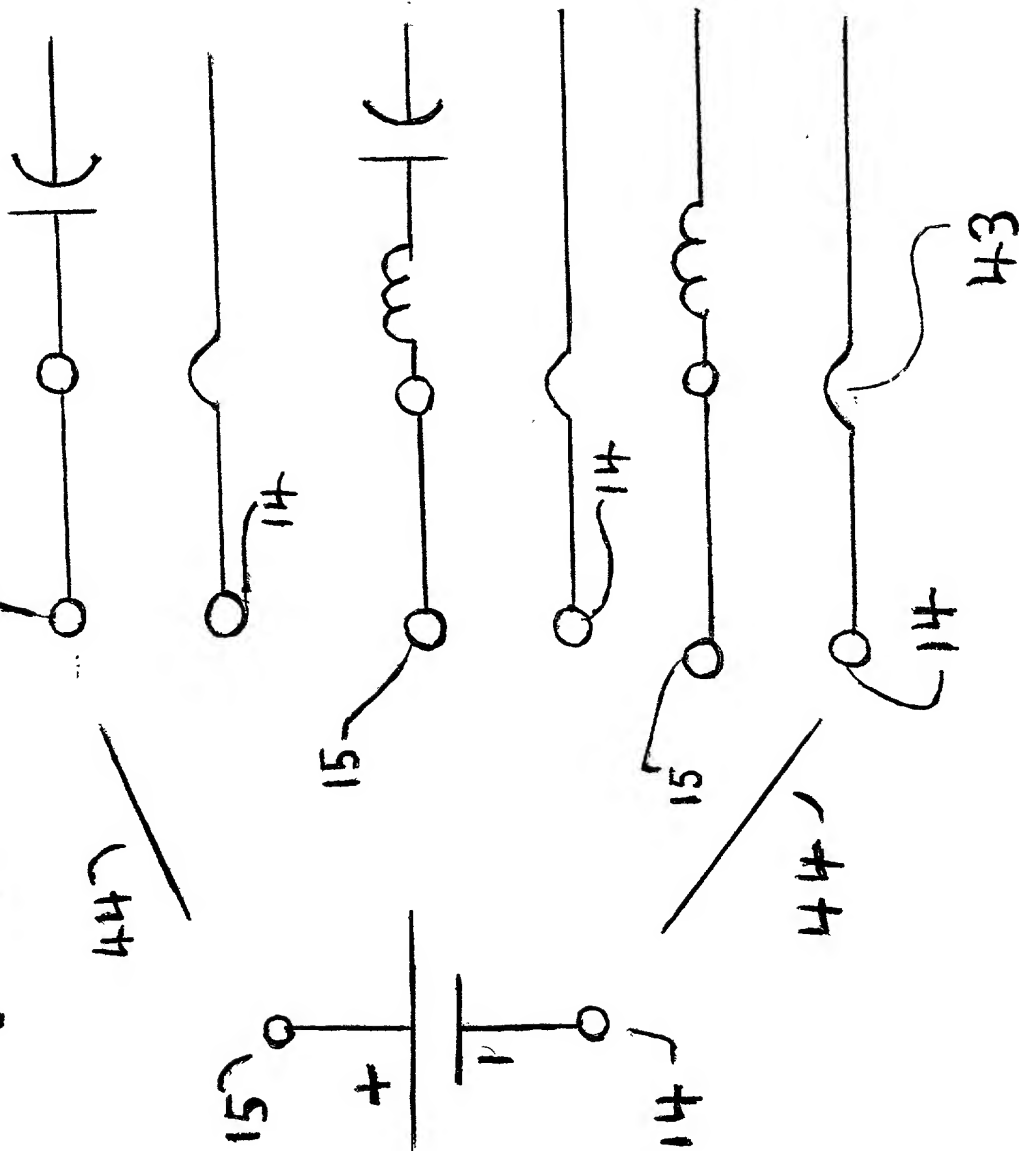
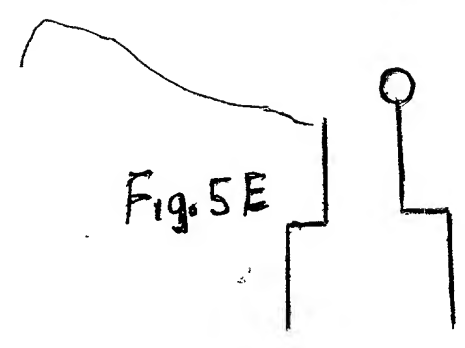
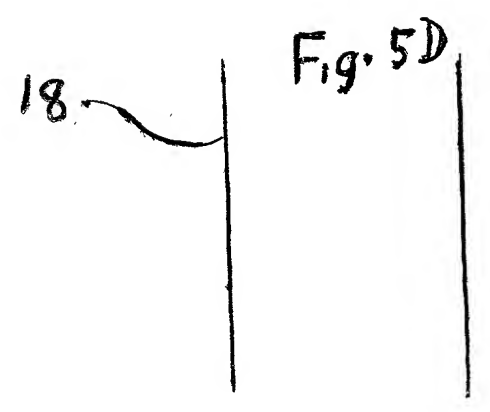
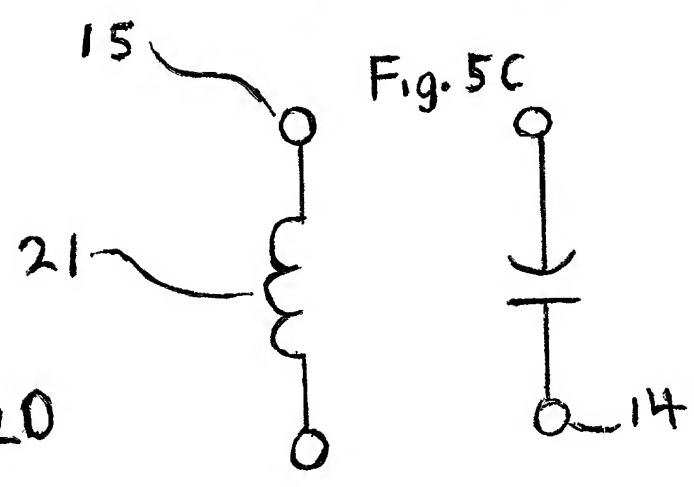
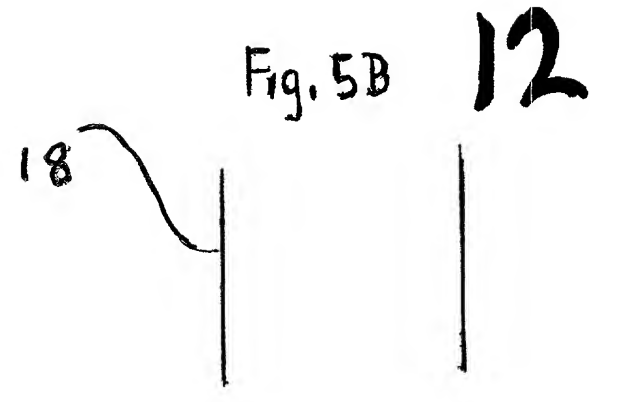
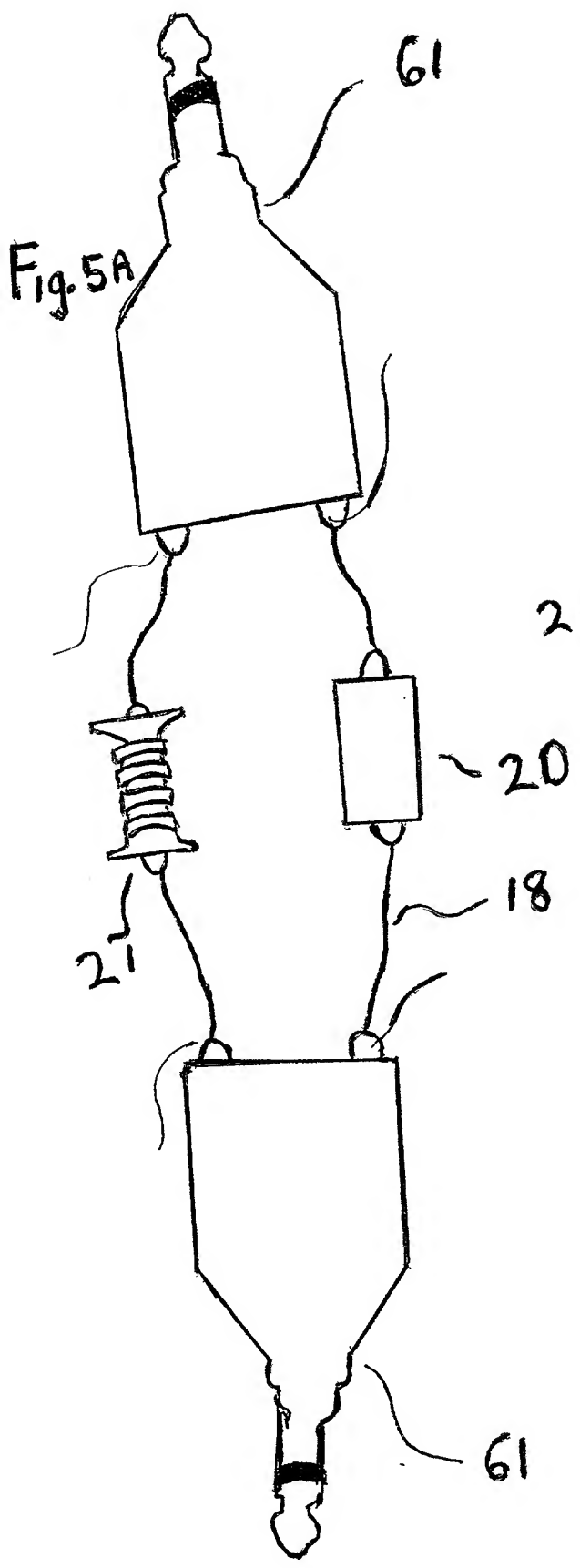


Fig. 4D





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Fig. 5 F

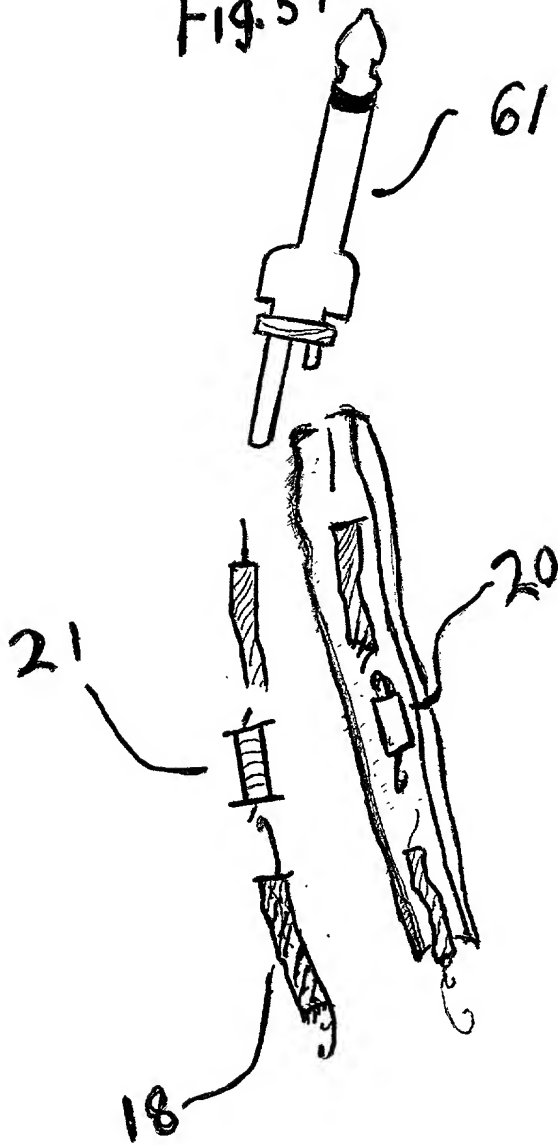
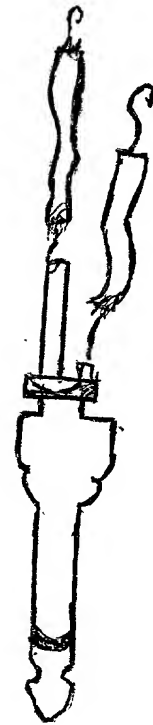
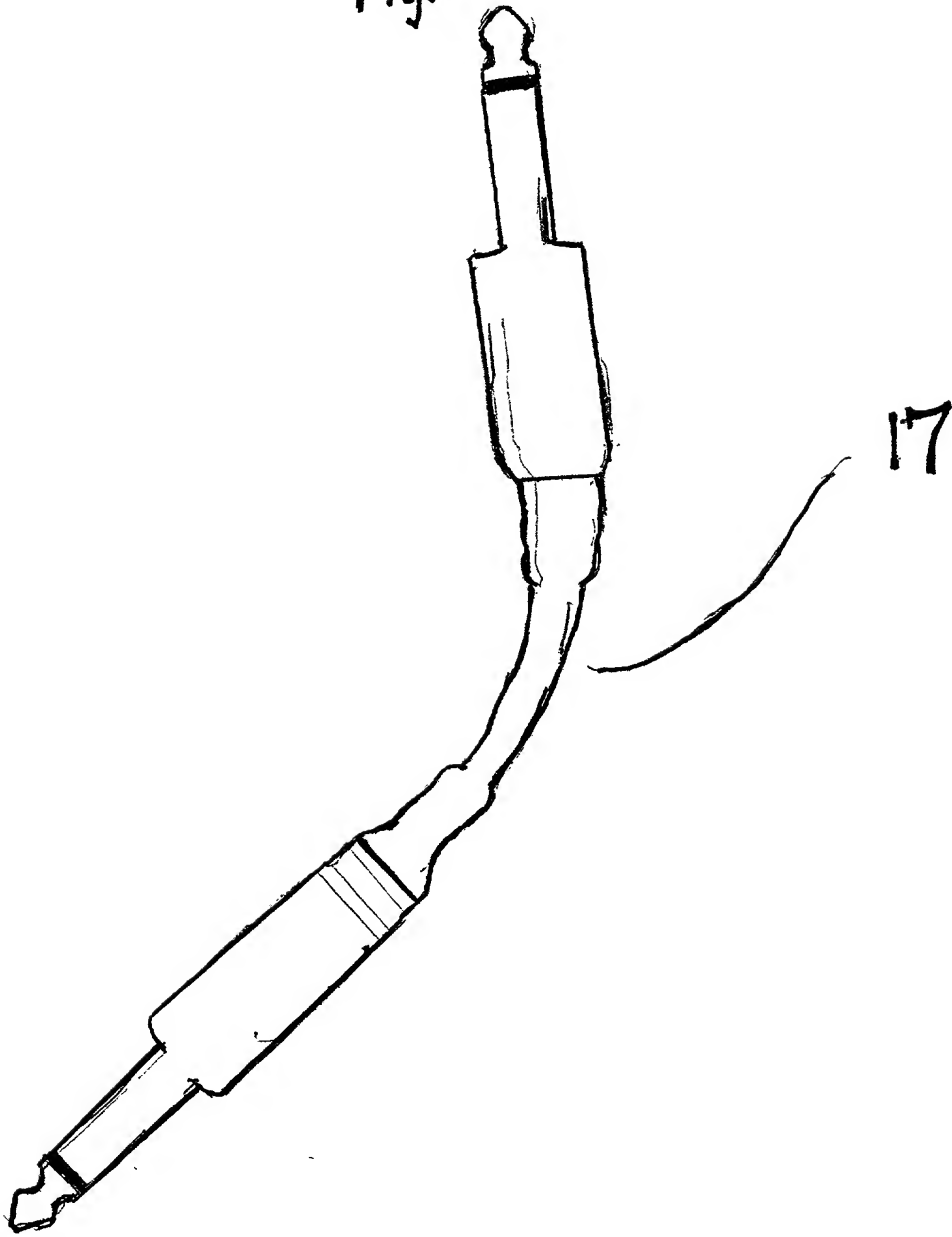


Fig. 5 G



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Fig. 5 H



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Fig. 6C

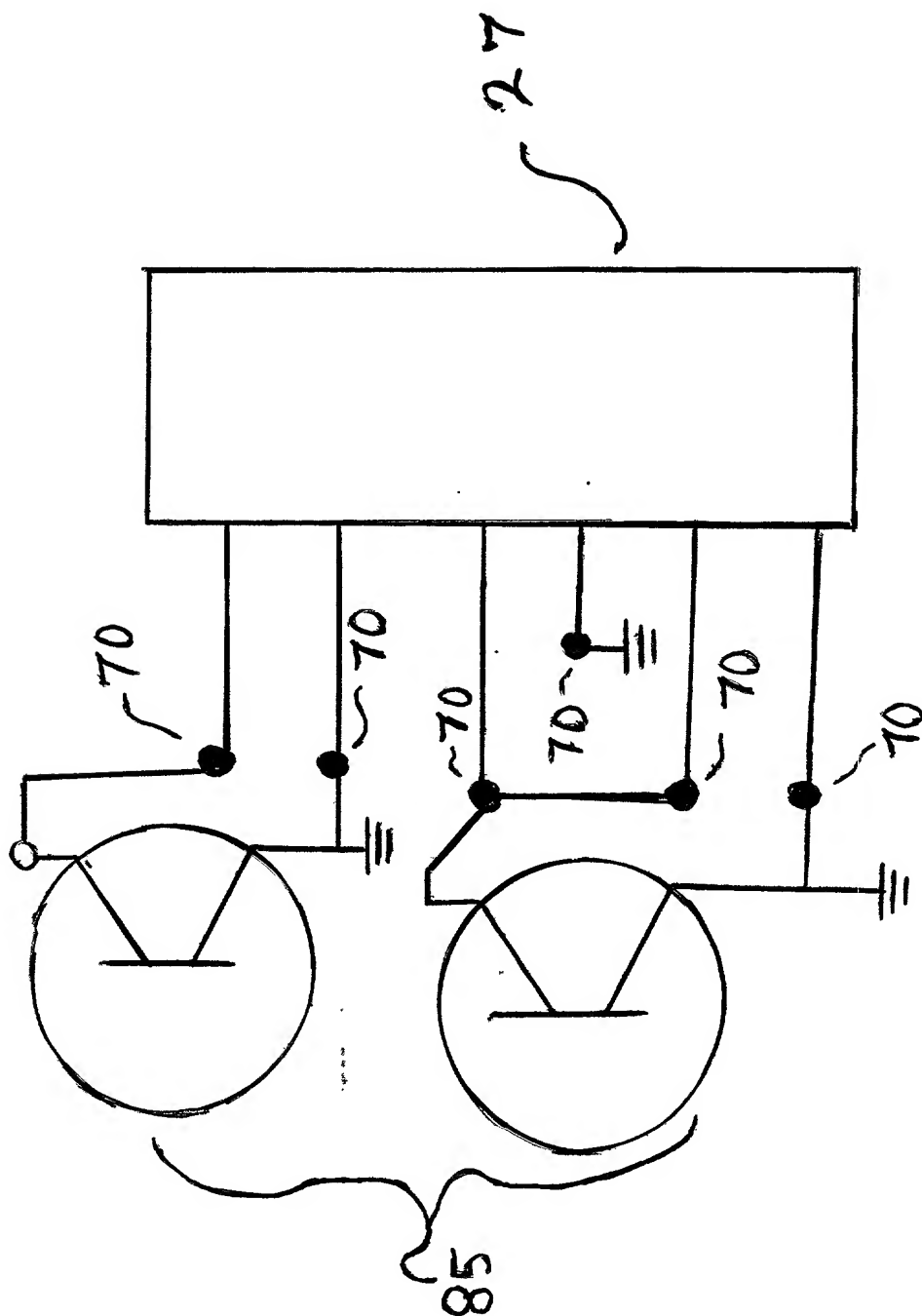
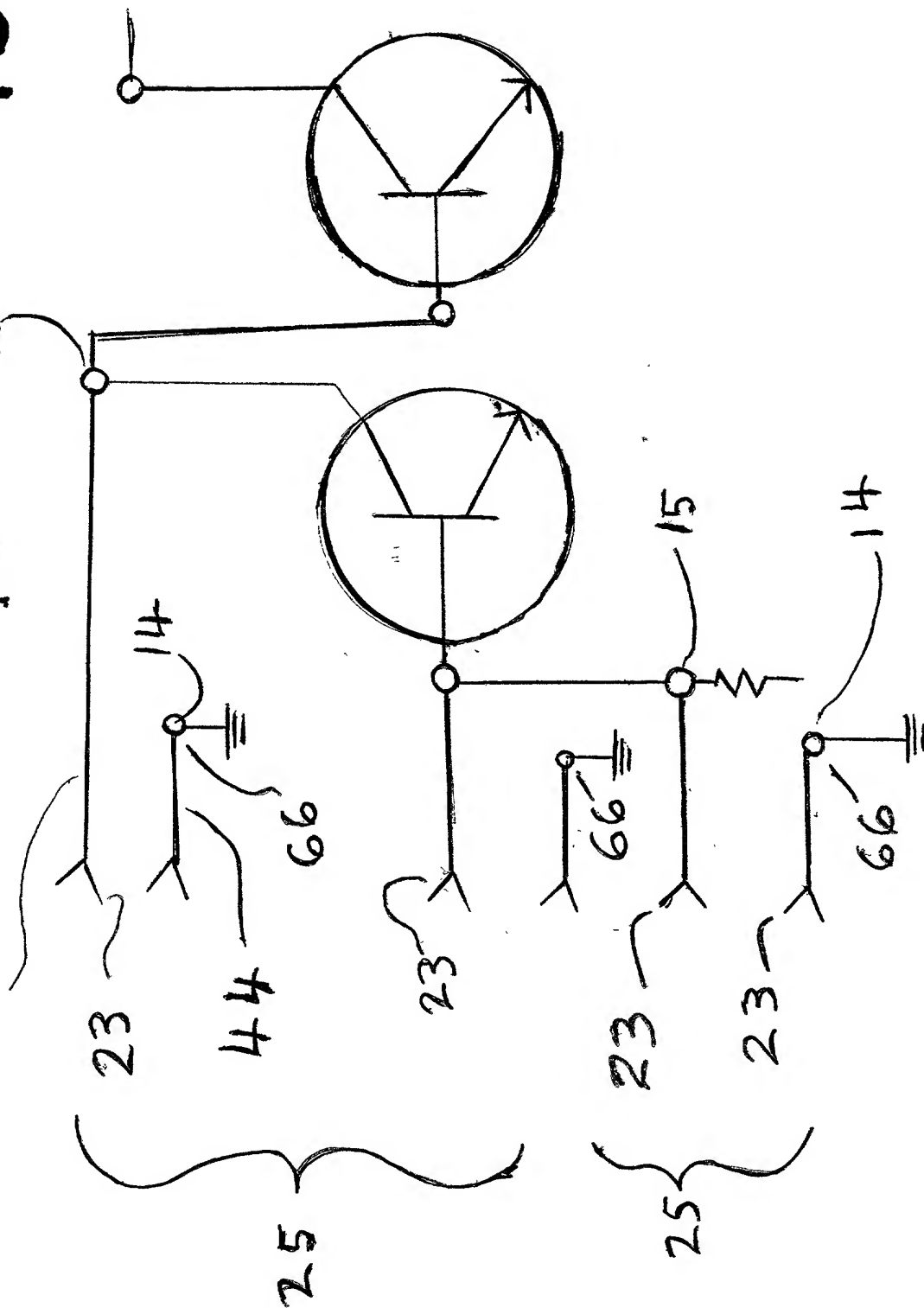
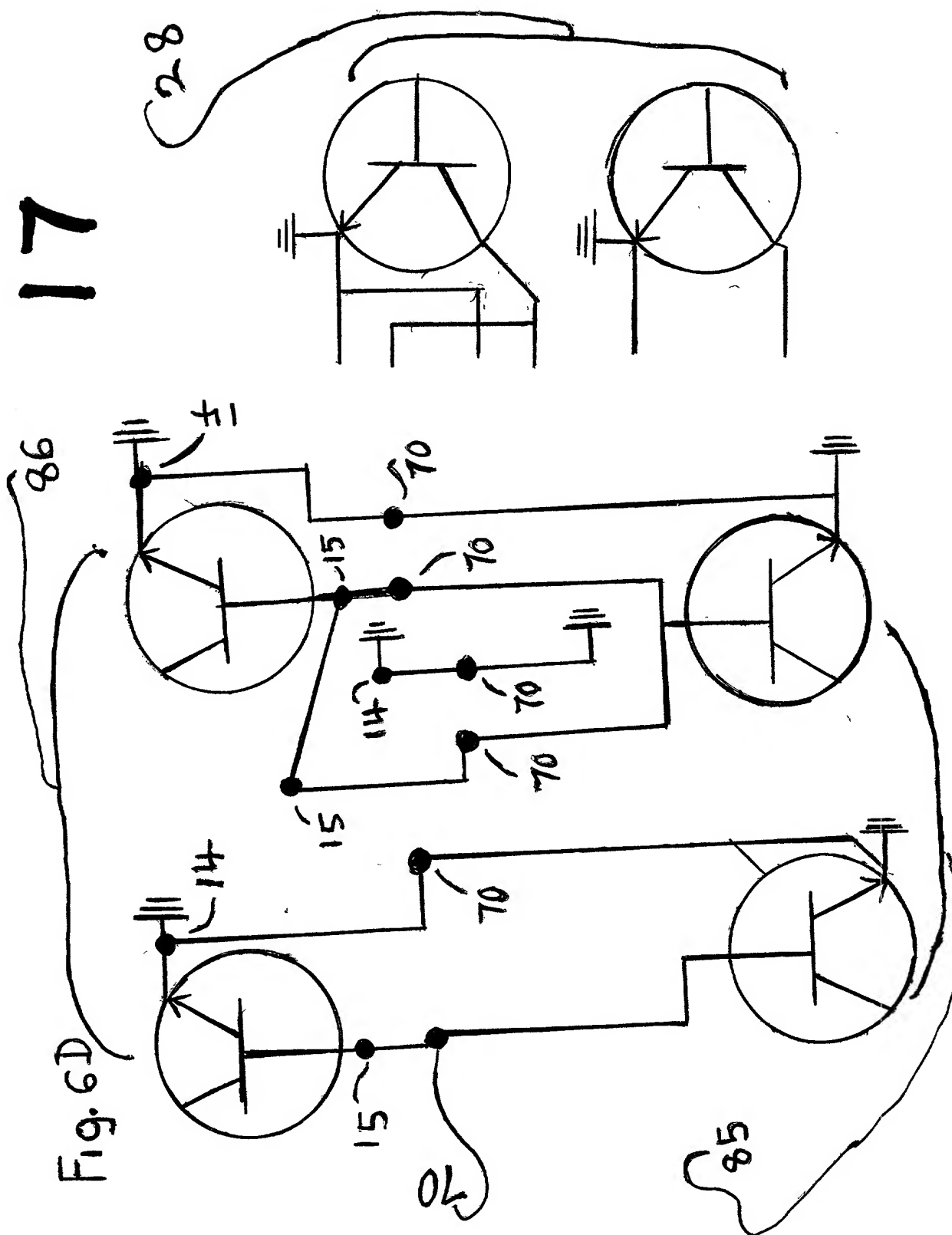


Fig. 6A





18

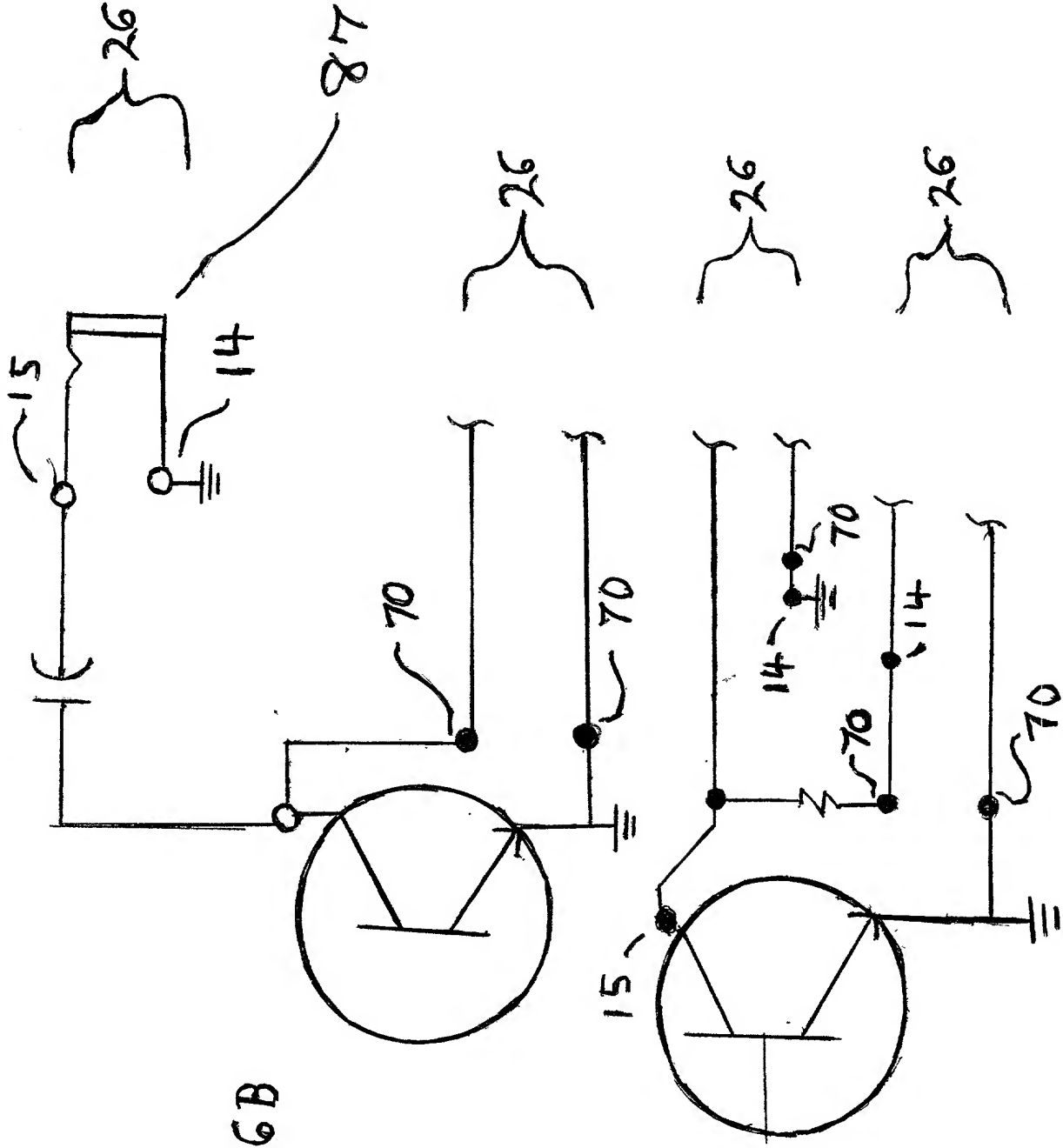


Fig. 6B

Fig 4A

19

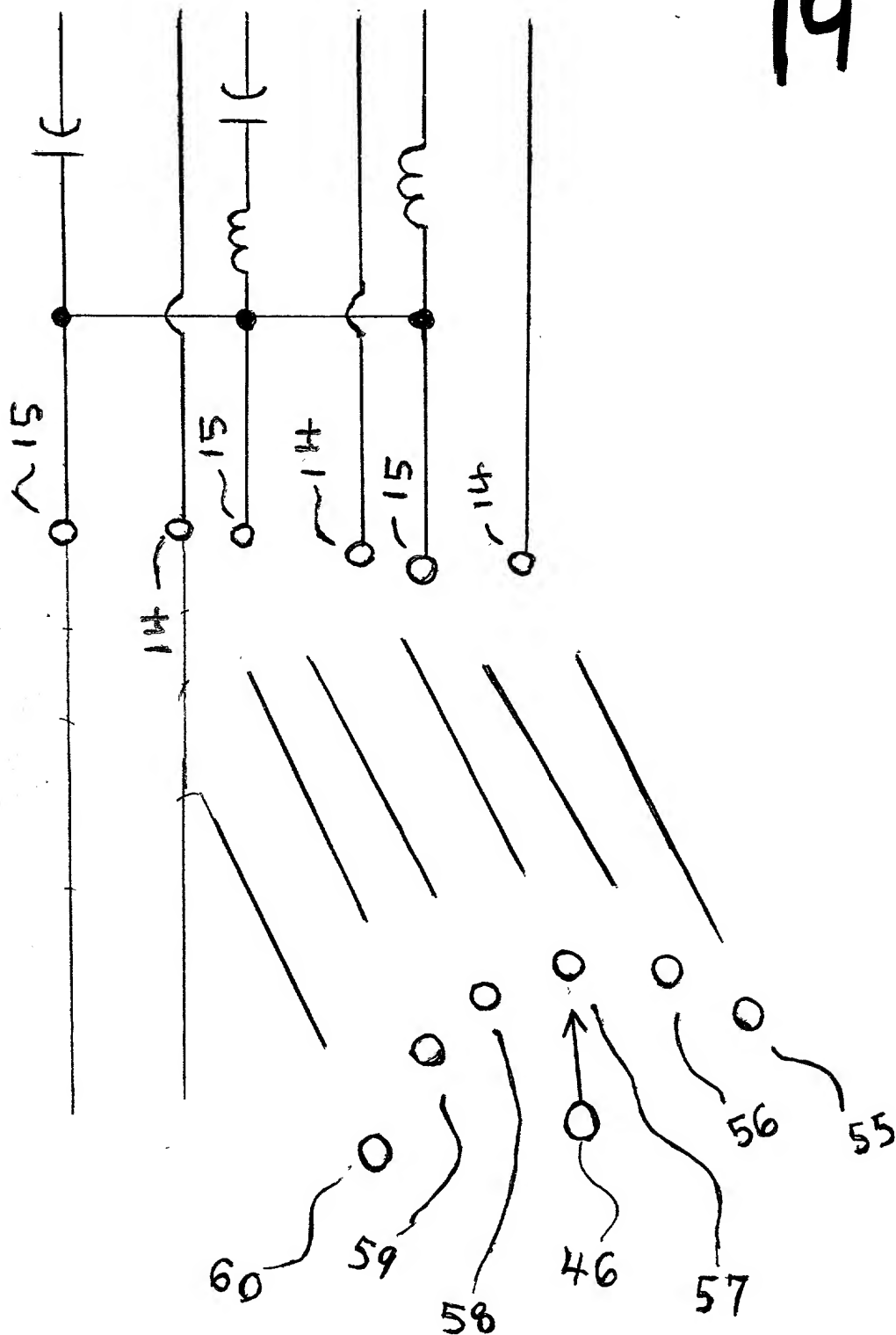
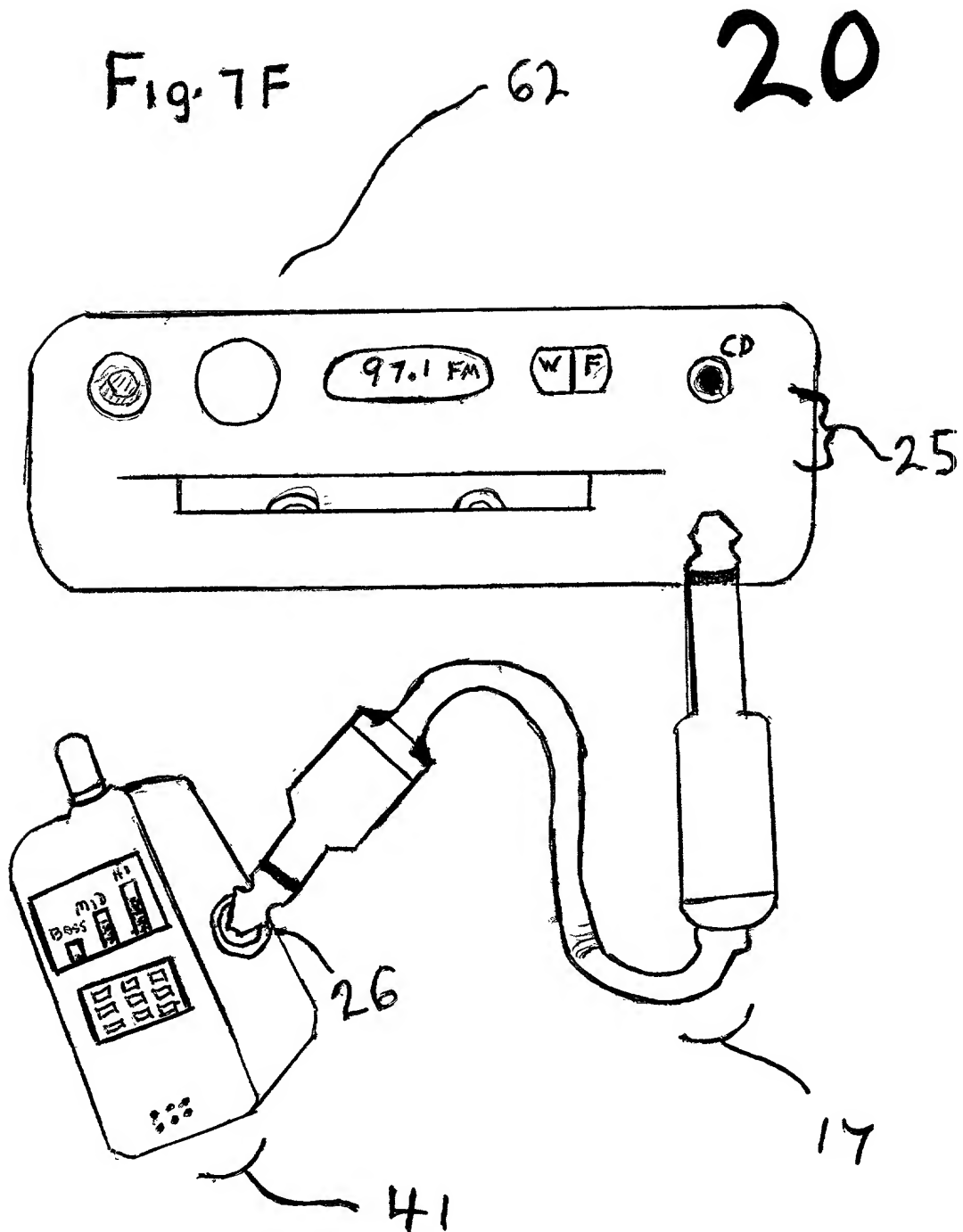


Fig. 7F



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Fig. 7A

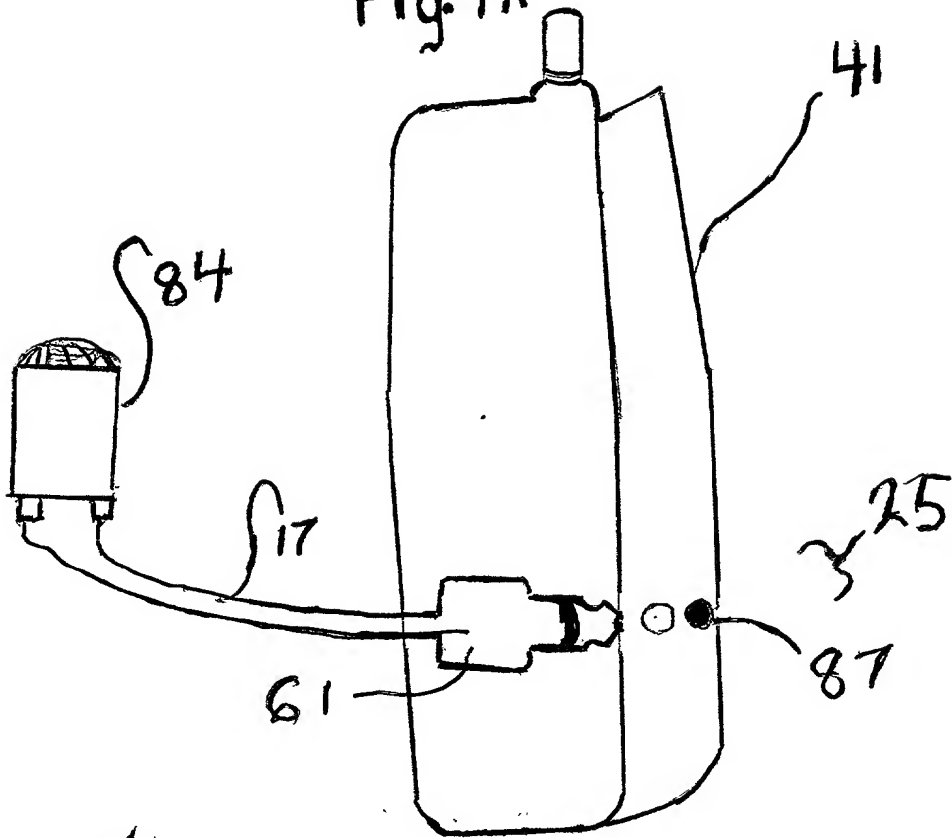
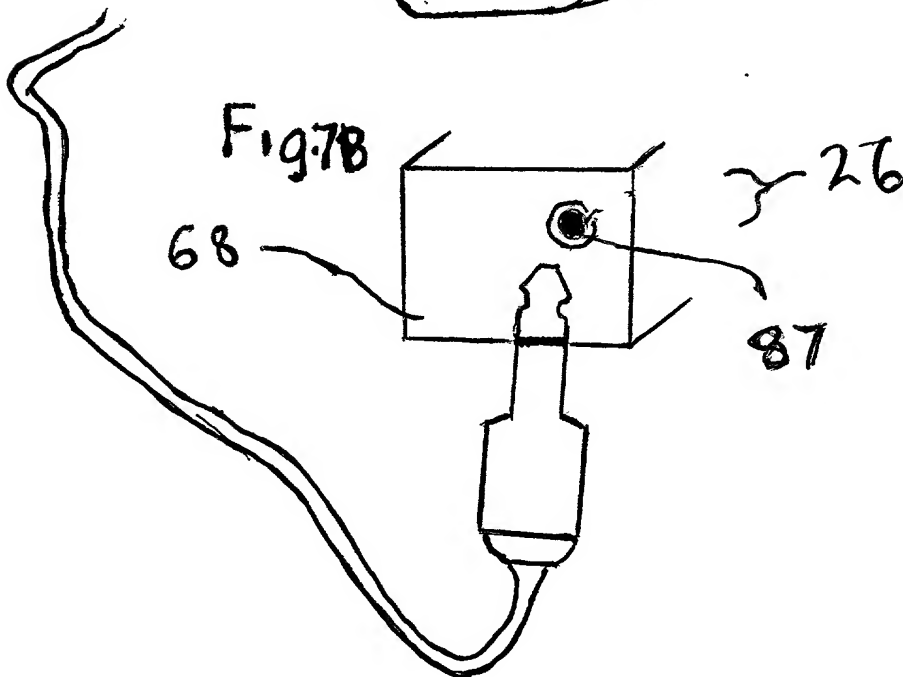
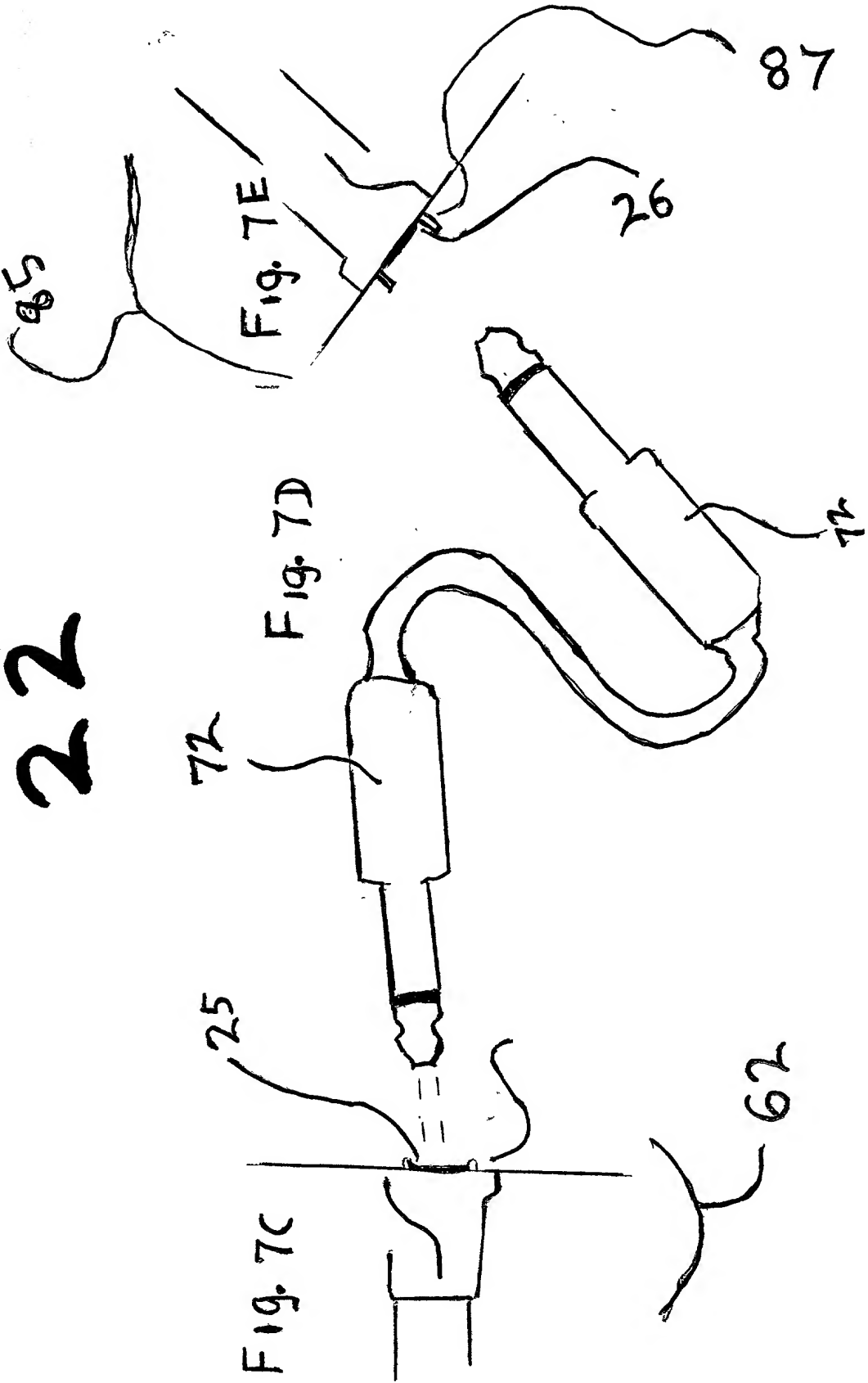
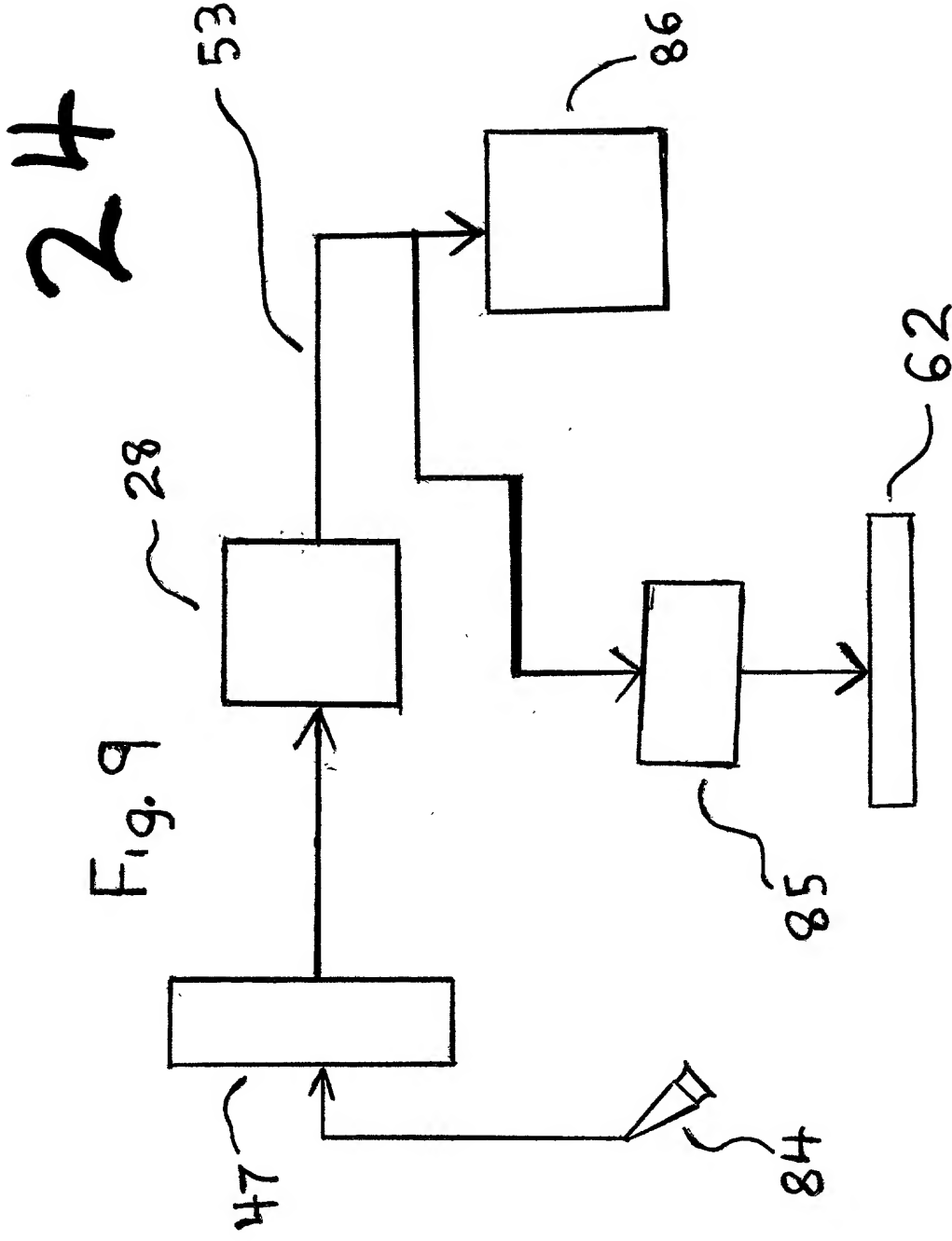


Fig. 7B



22





25

Fig. 10A

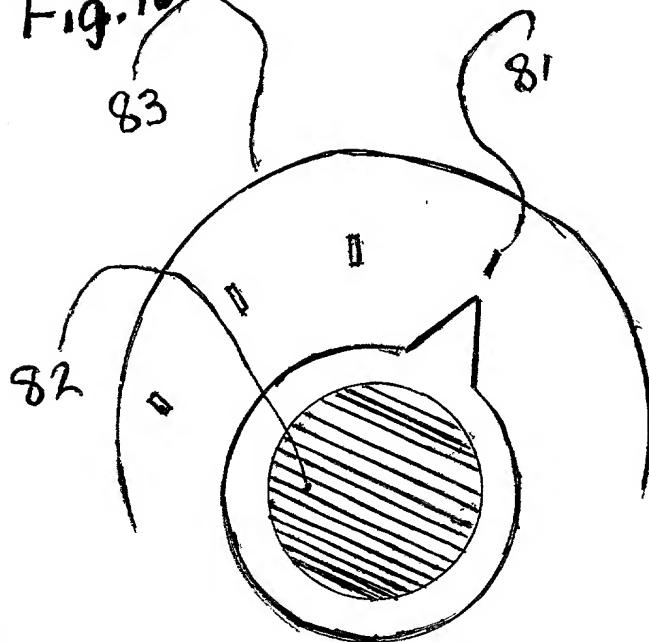
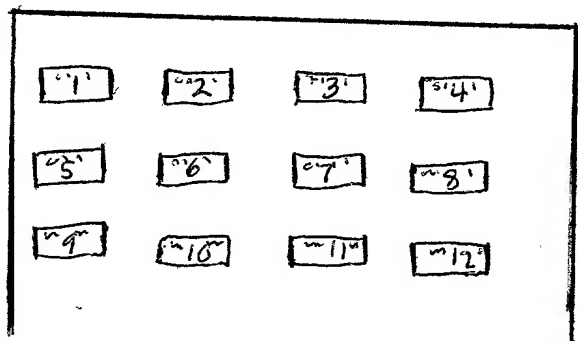
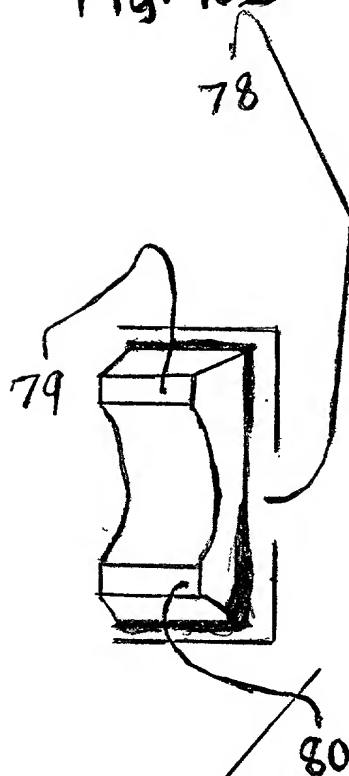


Fig. 10B



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Fig. 8

